

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	2757993
<b>Application Number:</b>	10612061
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	3549
<b>Title of Invention:</b>	Method and apparatus for rearranging data between multiple registers
<b>First Named Inventor/Applicant Name:</b>	Eric L. Debes
<b>Customer Number:</b>	59796
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<b>Filer Authorized By:</b>	
<b>Attorney Docket Number:</b>	42P15765
<b>Receipt Date:</b>	23-JAN-2008
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 180
RAM confirmation Number	3440
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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	42P15765_Amnd_1-23-08.pdf	52018 db31e42edfaa1ca1d9fb36762051d3dd 2114578b	no	19
<b>Warnings:</b>					
<b>Information:</b>					
2	Information Disclosure Statement (IDS) Filed	P15765_IDS_1-23-08.pdf	1923712 9b20ae8378c3e3032e1e4d1699e6a42ae 8c2844e0	no	6
<b>Warnings:</b>					
<b>Information:</b>					
3	NPL Documents	TM_1000.pdf	1989837 79ae50f732763de78832087aaf070de1 9167aa64	no	40
<b>Warnings:</b>					
<b>Information:</b>					
4	NPL Documents	MIPS_Digital_Media_Ext.pdf	907275 0240aeef3a1c365462b31a247358282 e17415a42	no	40
<b>Warnings:</b>					
<b>Information:</b>					
5	NPL Documents	64-bit_and_Multimedia_Extensions.pdf	854063 1509c0b1699b20ee12e08f8a8b65c2 5a959e81	no	17
<b>Warnings:</b>					
<b>Information:</b>					
6	NPL Documents	UltraSPARC.pdf	610874 db5e7c4e79356c07a210d1770ee4a1a0 a544762a	no	8
<b>Warnings:</b>					
<b>Information:</b>					
7	NPL Documents	1980KawakamiYFebIEEIS SCC.pdf	790874 133dc1d8f11268b1af3a3d4c0eb3e8204c 6a9855	no	2
<b>Warnings:</b>					
<b>Information:</b>					
8	NPL Documents	Philips_Hopes_Displace_DS Ps_w_VLIW.pdf	746285 4d9ec26a0f9834845d48eae54509d997 3db94c0e	no	7

<b>Warnings:</b>					
<b>Information:</b>					
9	NPL Documents	New_PA-RISC_Proc_Decodes_MPEG.pdf	226484 4b0b999a4bea21a00e73a19f030a2c8045c1e037	no	2
<b>Warnings:</b>					
<b>Information:</b>					
10	NPL Documents	TMS320c2xUsersGuide.pdf	7205029 3e1458ff42d78a2b2c3632a848ba46c3e5b35c4c	no	47
<b>Warnings:</b>					
<b>Information:</b>					
11	NPL Documents	i860_Microprocessor_Ref.pdf	15734020 d87a0bcca0d066d1b0b33731305706171432a1	no	109
<b>Warnings:</b>					
<b>Information:</b>					
12	NPL Documents	1995LeeRAprIEEMicro.pdf	1622715 3f437b69430ac335274873a5d57b731d22a3334	no	11
<b>Warnings:</b>					
<b>Information:</b>					
13	NPL Documents	Pentium_Proc_UseR_Man.pdf	7691651 8ed554a3d9e1d4d8f7731515b614a719048cd0	no	169
<b>Warnings:</b>					
<b>Information:</b>					
14	NPL Documents	i860_Microprocessor_Architecture.pdf	8621848 b7feeab59f1c5d95a77b753176815c7bcb452	no	170
<b>Warnings:</b>					
<b>Information:</b>					
15	NPL Documents	Intel_i750_i860tm_i960Proc.pdf	422621 02346b6860d560d76f6575f5119431a5739b5c05	no	3
<b>Warnings:</b>					
<b>Information:</b>					
16	Fee Worksheet (PTO-06)	fee-info.pdf	8175 80e1ec0a0df05a6d3a1c116da4625d5896020a0c	no	2
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<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			49407481		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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